

## GENERAL INFORMATION

### 1. PARTICIPANTS OF THE 7<sup>th</sup> WORLD COMPUTER-CHESS CHAMPIONSHIP

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## 2. THE COMPETITORS OF THE 7<sup>th</sup> WORLD COMPUTER-CHESS CHAMPIONSHIP

Program	Language	Code + Data K bytes	Book x1000	Computer	Nodes/sec x1000
Chessmachine Schröder Zugzwang	assembler	64+32	40	arm-2 32 MHz	6-7
	occam	170+3500 (per node!)	40	1024 T805	250
Cumulus 2	assembler	n.a.	10	80486 66 MHz	15
	assembler	70+30	128	Sparc CPU 51 MHz	20
Fritz2	assembler	n.a.	28	80486 66 MHz	20
Saitek Risc 2500	C+assembler	55+30	39	arm-2 28 MHz	3
Hitech B*	C+microcode	n.a.	n.a.	Spec. purpose hardware	120
Chess Genius	assembler	n.a.	150	80486 50 MHz	15
Woodpusher	C	250+6000	39	80486 66 MHz	3.5
M_Chess Pro	C+assembler	n.a.	200	80486 66 MHz	5.5
Hiarcs	C	200+200	8	Sun Sparc 2	3
Lachex	fortran+Cray ass.	n.a.	n.a.	Cray YMP-2E	40
Pandix	C+assembler	32+64	60	80486 66 MHz	4-15
Kallisto	assembler	n.a.	500	80486 50 MHz	10
Ulysses	C	350+2000	5	Sun Sparc 2	5
Prochess	C	128+128	25	80486 50 MHz	10-15
Centaur	Pascal	210+140	35	80486 66 MHz	0.05
Nimzo-Guernica	C+assembler	130+6000	90	80486 50 MHz	5
Echec	C	100+4000	15	80486 66 MHz	5
Nightmare	C	100+400	40	80486 66 MHz	6
Mirage	assembler	30+20	10	80486 66 MHz	n/a
Delicate Brute	C	1000	10	Sun Sparc 2	10



Photo by Jos Uiterwijk

ED WILLING, THE RACE IS NOT TO THE SWIFT.

Ed Schröder, the World Computer-Chess Champion, with the Shannon trophy (Madrid, November 27, 1992).